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Drawings

4/8/03


October 3, 2001

TO: Commissioner of Patents and Trademarks
Washington, D.C. 20231

FROM: George O. Saile, Reg. No. 19,572
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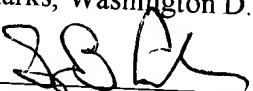
Subject: Serial #09/896,486
File Date: 6/29/01
Invr(s): Mendis, S.K., et al
Title: An On-Chip Design-For-Testing Structure For CMOS APS (Active Pixel Sensor) Image Sensor

Enclosed are twelve sheets of Formal Drawing. Please enter these Drawings to replace the informal Drawings that were submitted with the Patent Application on 6/29/01. The Drawings should be filed under the Serial # 09/896,486. No new matter is added at this time. Thank you.


Stephen B. Ackerman
Reg. No. 37,761

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington D.C. 20231 on October 3, 2001

Signature 
Stephen B. Ackerman, Reg. 37,761

Date: 10/3/01